

Fig.2

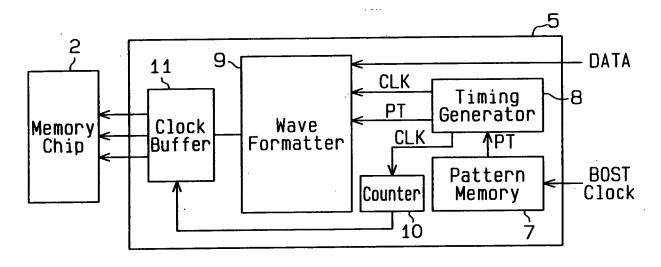


Fig.3

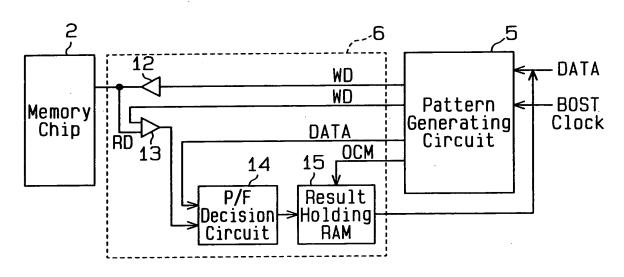


Fig.4

٧, **₹** BOST-IN 18 16 \ SS 일 | | / 100 100 Memory Chip \ss വ

Fig.5

Fig.6

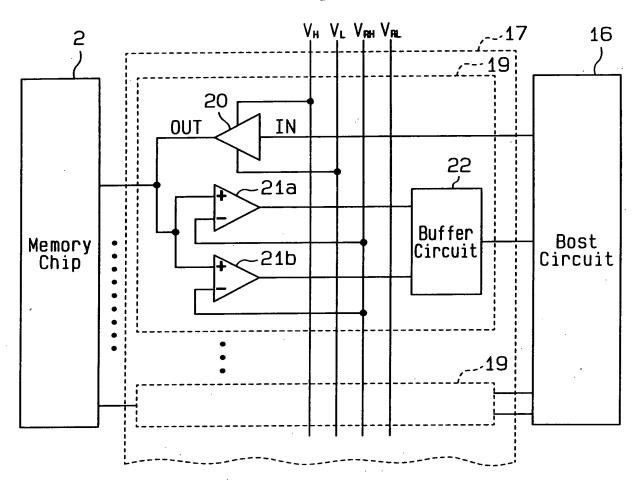
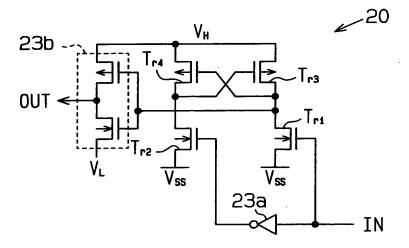


Fig.7



F. Q.

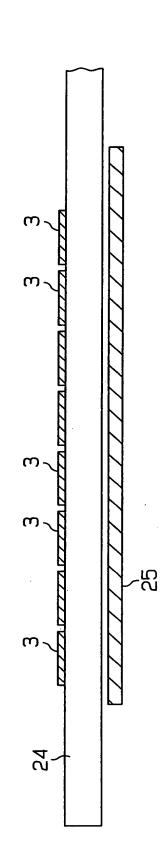


Fig.9

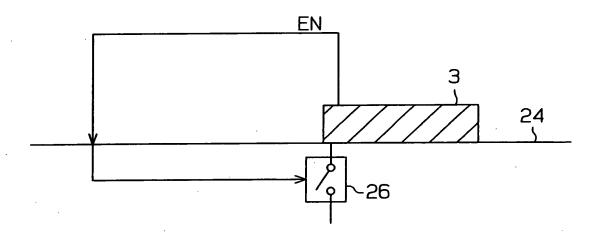
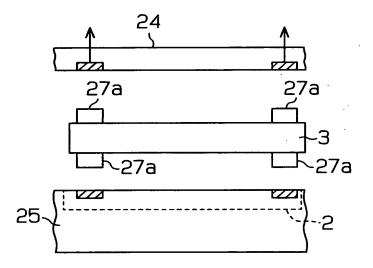


Fig.10



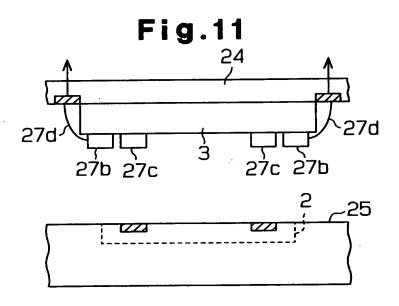
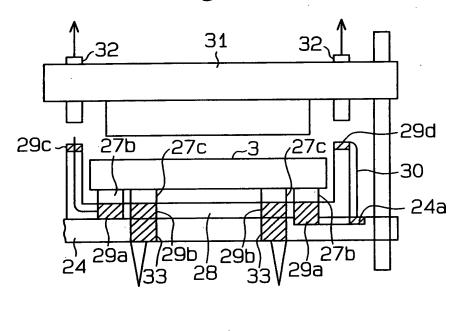


Fig.12



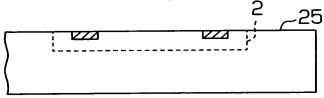


Fig.13

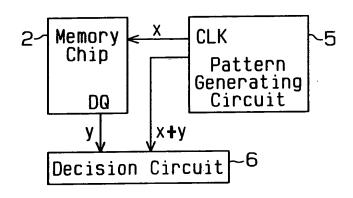


Fig.14 34a High Frequency Generator 37 Latch Circuit CLK 35 Selection Circuit Frequency Counter 36 34b Latch Access Time Determining Circuit DQ Circuit -38

Fig.15

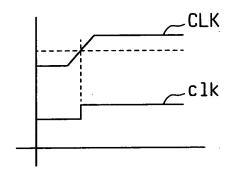


Fig.16

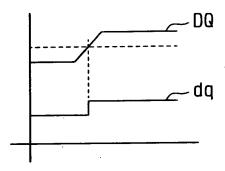


Fig.17

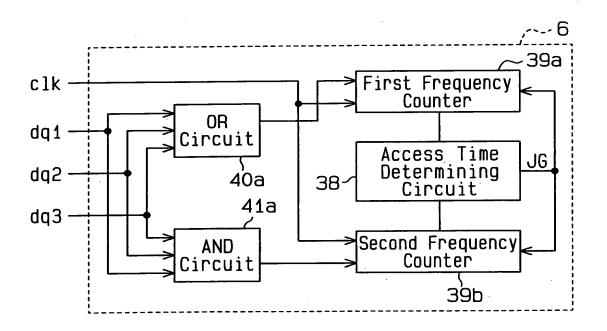


Fig.18

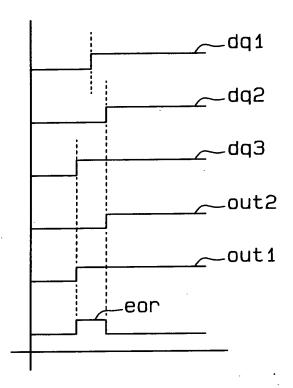


Fig.19

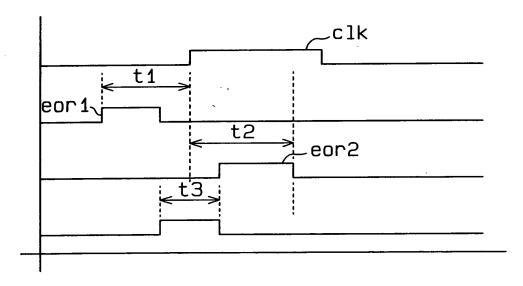


Fig.20

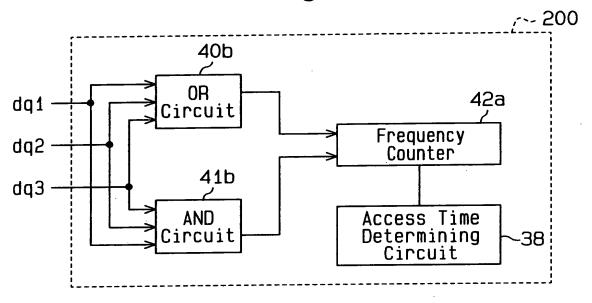


Fig.21

